

Faculty Profile

Faculty Name	Sapna
Designation	Assistant Professor
Qualification	M.Tech, B.Tech
Email	sapna@gecnilokheri.ac.in
Area of Interest	Communication System, Digital System Design
Work Experience (Total)	7 Years
<ul style="list-style-type: none"> Teaching 	7 Years
Courses taught at Diploma/ Post Diploma/ Under Graduate/ Post Graduate/ Post Graduate Diploma Level	Digital Electronics, Analog Communication, Analog Circuit, Mixed Signal Design, Microprocessor, Power Electronics.
Research Publications	2
<ul style="list-style-type: none"> Research Paper UGC-CARE 	2
<ul style="list-style-type: none"> List of Publications 	Enclosed as Annexure 1

List of Publications:- ANNEXURE 1

- Sapna Last Name. *"Different Aspects and Reviews on Steganography for Data Hiding"*, Volume 3, Issue VI, International Journal for Research in Applied Science and Engineering Technology (IJRASET) Page No: 487-491, ISSN : 2321-9653, www.ijraset.com.
- Sapna "Video Steganography through RC4 Cryptography with Random Sequence of Data Hiding", <https://ijircce.com/article/video-steganography-through-rc4-cryptography-with-random-sequence-of-data-hidingsapna-10988>